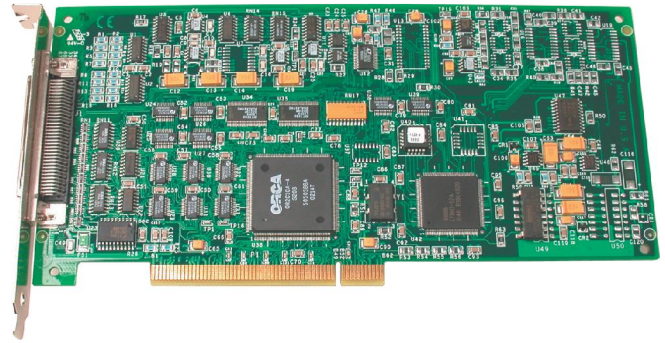


DT300 Series

Multifunction PCI Data Acquisition Boards



By sharing a common architecture and making use of standard accessories and software, the DT300 Series provides a full range of cost-effective options to meet your data acquisition needs both now and in the future.

Key Features

- Six different board configurations provide a range of flexible, cost-effective options
- High-speed bus-master transfers data without CPU intervention freeing up system resources for processing
- ENOB specification guarantees outstanding total system accuracy
- High-speed dynamic digital inputs can time stamp your digital input in relation to A/D
- Generous 1024-location channel gain list allows flexible channel selection in any sequence desired
- Flexible triggering and clocking capabilities
- No interrupts required, freeing up system resources
- Included software and drivers

Supported Operating Systems

- Windows® 10/8/7/Vista®/XP 32/64-bit

Multifunction Data Acquisition Boards

Each DT300 Series board contains 16 single-ended (SE) or 8 differential (DIFF) analog inputs, 23 digital I/O lines, and four counter/timer channels. The resolution and throughput of the analog input channels differ depending on the model. The DT302, DT304, and DT322 boards also provide 2 analog output channels. With the plug-and-play feature of the PCI bus, no jumpers or switches to set, and the common architecture, accessories, and software of the DT300 Series, installing and upgrading DT300 Series boards is easy.

DT300 Series is a family of low-cost, multifunction PCI bus-mastering boards.

Analog Inputs

All DT300 Series boards feature 16 SE or 8 DIFF inputs. In addition, an Amp Low connection allows SE inputs to be referenced to a common point other than ground to provide 16 pseudo-DIFF inputs. Software-selectable unipolar or bipolar operation and gain settings of 1, 2, 4 or 8 provide input ranges of 0-10 V, 0-5 V, 0-2.5 V, 0-1.25 V, ± 10 V, ± 5 V, ± 2.5 V, and ± 1.25 V. For added flexibility, a 1024-location channel-gain list allows you to sample non-sequential channels and channels with different gains. The A/D resolution and maximum sample rate vary depending on the board type, as listed below:

Board	Resolution	Sample Rate
DT301/302	12-bit	225 kS/s
DT303/304	12-bit	400 kS/s

By offering board types with different features, the DT300 Series provides the right cost-effective solution for your data acquisition needs. The analog input subsystem can be completely software calibrated for hands-off operation.

Features Summary									
Board	Analog Inputs				Analog Outputs			Digital I/O	
	Channels	Resolution	Input Ranges	Sample Rate	Channels	Resolution	Output Ranges	I/O Lines	Counter/Timer
DT301	16 SE/8 DIFF	12-bit	± 1.25 V, 2.5 V, 5 V, 10 V, 0-1.25 V, 2.5 V, 5 V, 10 V	225 kS/s	—	—	—	23	4
DT302	16 SE/8 DIFF	12-bit	± 1.25 V, 2.5 V, 5 V, 10 V, 0-1.25 V, 2.5 V, 5 V, 10 V	225 kS/s	2	12-bit	± 5 V, ± 10 V, 0-5 V, 0-10 V	23	4
DT303	16 SE/8 DIFF	12-bit	± 1.25 V, 2.5 V, 5 V, 10 V, 0-1.25 V, 2.5 V, 5 V, 10 V	400 kS/s	—	—	—	23	4
DT304	16 SE/8 DIFF	12-bit	± 1.25 V, 2.5 V, 5 V, 10 V, 0-1.25 V, 2.5 V, 5 V, 10 V	400 kS/s	2	12-bit	± 5 V, ± 10 V, 0-5 V, 0-10 V	23	4
DT321	16 SE/8 DIFF	16-bit	± 1.25 V, 2.5 V, 5 V, 10 V,	250 kS/s	—	—	—	23	4
DT322	16 SE/8 DIFF	16-bit	± 1.25 V, 2.5 V, 5 V, 10 V,	250 kS/s	2	16-bit	± 10 V	23	4



Analog Input Acquisition Modes

DT300 Series boards can acquire a single value from any channel or a number of samples from multiple channels. To acquire data from multiple channels, DT300 Series boards provide two scan modes: continuously paced and triggered scan mode. Using continuously paced mode, the board scans the channel-gain list continuously and acquires data until you stop the operation or until a specific number of samples is acquired.

Using triggered scan mode, the board scans the channel-gain list at high speeds with a programmed interval between scans, emulating a simultaneous sample-and-hold operation. Using an external or internal trigger source, you can retrigger a triggered scan operation to cycle through the channel-gain list up to 256 times per trigger; this allows you to acquire a waveform of data per channel for each trigger, up to 256 kilosamples per trigger. You can pace either scan mode using an internal or external clock.

Flexible Triggering and Clocking Capabilities

Clocking and triggering of the A/D system in DT300 Series boards is provided by two 24-bit counter/timers with a 20 MHz oscillator. Four additional 16-bit counter/timers are available to the user.

The DT300 Series provides flexibility in triggering, both in the trigger modes available and the events that can cause the trigger. Trigger modes include post-trigger, pre-trigger and about-trigger acquisitions.

In post-trigger mode, the board acquires data after a digital (TTL) trigger or software trigger. In pre-trigger mode, acquisition proceeds until a digital trigger occurs. In about-trigger mode, the board acquires data both before and after a digital trigger occurs.

ENOB Specification Measures Total Accuracy

Total system performance of the DT300 Series is specified with the most accurate measurement available: ENOB (Effective Number of Bits). Derived from a board's signal-to-noise ratio, ENOB specifies the overall accuracy of the A/D transfer function. This single comprehensive figure of merit enables you to easily evaluate a board's AC accuracy and performance. ENOB is 11.5 bits for the DT301, DT302, DT303, and DT304, and 14 bits for the DT321 and DT322.

You can pace the analog inputs using an internal or an external clock. You can set the internal clock to acquire data from one or more channels from 1.2 S/s up to the maximum sample rate of the board. If slower rates are desired you can use an external source, or cascade two or more of the user counter/timers and connect the output to the external clock input.

The external A/D sample clock and the digital trigger input signals are accessible through the user connector.

Analog Outputs

The DT302, DT304, and DT322 boards feature two serial, multiplying, analog output channels. The output rate for each channel is 10 kS/s (typical), based on a single-value, polled operation. The resolution and software selectable output range options for each board are listed below:

Board	Resolution	Output Ranges
DT302	12-bit	±5 V, ±10 V, 0-5 V, 0-10 V
DT304	12-bit	±5 V, ±10 V, 0-5 V, 0-10 V
DT322	16-bit	±10 V

Digital I/O

All DT300 Series boards feature 23 digital I/O lines. These lines are divided into the following ports:

• Ports A, lines 0 to 7	• Port C, lines 0 to 6
• Port B, lines 0 to 7	

Using software, Ports A and B can be combined to create a virtual port consisting of 16 digital I/O lines. You can program each port for input or output and read or write to the port.

High-Speed Dynamic Digital Inputs

You can also read the status of the Ports A and B at rates as high as 3 MS/s by including the digital input lines of Ports A and B as a channel in the analog channel-gain list. This dynamic digital input feature allows you to *time stamp* the digital inputs in relation to the analog inputs. In this mode, all 16 digital input lines of Ports A and B are read as one word. The digital outputs have sufficient current capability to drive external solid-state relay modules (sink 12 mA and source 15 mA).

User/Counter Timers

Four 16-bit user counter/timers are available for counting events, creating a one-shot or frequency output, or measuring a frequency input. You can cascade two counters internally through software or cascade more than two counters externally on the screw terminal accessory. You can also set the duty cycle, frequency, and output polarity of the output pulse from the user counter/timers.

These four user counter/timers are in addition to the two 24-bit counter timers dedicated to clocking and triggering in the A/D system.



PLCs



Acquisition



Instruments



Data logger



Power



HMIs



Switches



Motion



Sensors



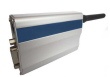
Converters



Keyboards



SCADA



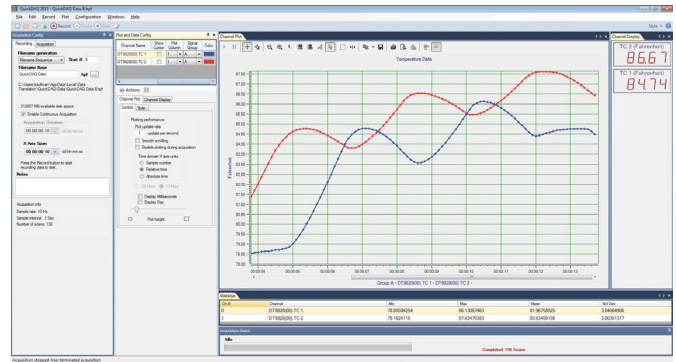
Telemetry

QuickDAQ

QuickDAQ allows you to acquire and display from all Data Translation data acquisition devices that support analog input streaming. Combine QuickDAQ with Data Translation hardware to acquire data, record data to disk, display the results in both a plot and digital display, and read a recorded data file. Be productive right out of the box with this powerful data logging software. Data can be exported to other applications like Microsoft Excel® and The Mathworks MATLAB® for more advanced analysis. Two additional options can be purchased to add FFT analysis capabilities to the base package.

Key Features

- **QuickDAQ Base Package (Free)**
 - Ready-to-measure application software
 - Configure, acquire, log, display, and analyze your data
 - Customize many aspects of the acquisition, display, and recording functions to suit your needs
- **FFT Analysis Option (License Required)**
 - Includes all the features of the QuickDAQ Base Package
 - Perform single-channel FFT operations including:
 - ◇ Auto Spectrum
 - ◇ Spectrum
 - ◇ Power Spectral Density
 - Configure and view dynamic performance statistics
- Supports Hanning, Hamming, Bartlett, Blackman, Blackman Harris, and Flat Top response windows
- **Advanced FFT Analysis Option (License Required)**
 - Includes all the features of the QuickDAQ Base Package and FFT Analysis Package
 - Perform 2-channel FFT operations including:
 - ◇ FRF
 - ◇ Cross-Spectrum
 - ◇ Cross Power Spectral Density
 - ◇ Coherence
 - ◇ Coherent Output Power
 - Supports real, imaginary, and Nyquist display functions
 - Additional FFT analysis functions supported: Exponential, Force, Cosiner Taper
 - Save data to .uff file format



QuickDAQ ships free-of-charge and allows you to get up and running quickly.

Other Software Options

The following software is available for free and provided on the Data Acquisition Omni CD:

- **Device Driver** –The device driver allows you to use the PCI DAQ board with any of the supported software packages or utilities.
- **DT-Open Layers® for .NET Class Library** – Use this class library if you want to use Visual C#® or Visual Basic® for .NET to develop application software using Visual Studio® 2003-2012; the class library complies with the DT-Open Layers standard.
- **DataAcq SDK** – Use the DataAcq SDK to use Visual Studio 6.0 and Microsoft® C or C++ to develop application software using Windows 10/8/7/Vista/XP 32/64-bit; the DataAcq SDK complies with the DT-Open Layers standard.
- **DAQ Adaptor for MATLAB** – Data Translation’s DAQ Adaptor provides an interface between the MATLAB® Data Acquisition (DAQ) toolbox from The MathWorks™ and Data Translation’s DT-Open Layers architecture.
- **LV-Link** – Data Translation’s LV-Link is a library of VIs that enable LabVIEW™ programmers to access the data acquisition features of DT-Open Layers compliant USB and PCI devices.



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User Connections

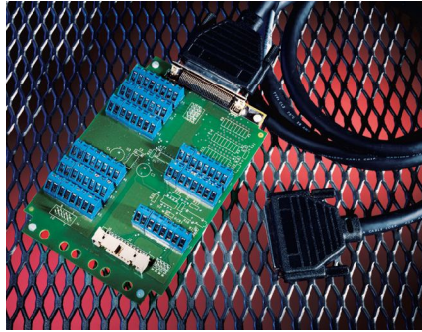
All signals are brought out to a dedicated 68-pin connector on the backplate of each DT300 Series board. The STP300 screw terminal panel is available to simplify connections. The EP305 cable connects the DT300 Series board to the STP300 screw terminal panel.

High-Speed Burst Data Transfer

A custom-designed PCI bus interface chip allows for high-speed, bus-mastering data transfers to the host computer. By setting aside a block of memory in the host computer, a DT300 Series board performs bus-master data transfers without CPU intervention, allowing the CPU to perform other tasks such as data analysis and graphics. This implementation does not carry the additional overhead of scatter-gather or other DMA transfer methods which necessitate reprogramming the controller for new addresses.

No Interrupts Required

Many data acquisition boards require interrupts. This uses up valuable system capability. DT300 Series boards require no interrupts, so installation is simple and valuable system resources are free for other uses.



STP300 Screw Terminal Panel and the EP305 Cable

The STP300 screw terminal panel together with the EP305 cable simplifies the connection of input and output devices to any DT300 Series board. Accommodating all user connections on convenient screw clamp connectors, the STP300 mounts on a panel and includes nylon standoffs for table-top applications. The panel measures 4.9 in. wide by 6.9 in. long. The EP305 is a 68-pin, 2 meter, shielded cable that connects the DT300 Series board to the STP300 screw terminal panel.

Cross-Series Compatibility

Virtually all Data Translation data acquisition modules are compatible with the DT-Open Layers for .NET Class Library. This means that if your application was developed with one of Data Translation's software products, you can easily upgrade to a new Data Translation board. Little or no reprogramming is needed.

Ordering Summary

HARDWARE

- **DT301** – PCI data acquisition board with 12-bit, 225 kS/s analog inputs and no analog outputs.
- **DT302** – PCI data acquisition board with 12-bit, 225 kS/s analog inputs and 2, 12-bit analog outputs.
- **DT303** – PCI data acquisition board with 12-bit, 400 kS/s analog inputs and no analog outputs.
- **DT304** – PCI data acquisition board with 12-bit, 400 kS/s analog inputs 2, 12-bit analog outputs.
- **DT321** – PCI data acquisition board with 16-bit, 250 kS/s analog inputs and no analog outputs.
- **DT322** – PCI data acquisition board with 16-bit, 250 kS/s analog inputs 2, 16-bit analog outputs.

ACCESSORIES

- **EP305** – 68-pin, 2 meter, shielded cable for connecting STP300
- **STP68** – Low-cost screw terminal panel
- **STP300** – Shielded screw terminal panel.

FREE SOFTWARE

- **QuickDAQ**
- **DAQ Adaptor for MATLAB** – Access the analysis and visualization tools of MATLAB®.
- **LV-Link** – Access the power of Data Translation boards through LabVIEW™.

OPTIONAL SOFTWARE

- **QuickDAQ FFT Analysis Option** (License Required)
- **QuickDAQ Advanced FFT Analysis Option** (License Required)



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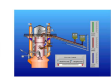
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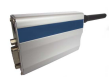
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